

**PATENT NUMBER**

<p><i>M.H.</i></p> <p>O.I.P.E.</p> <p>SCANNED <i>AGS</i> (2) O.A. <i>AA</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/717567	CONT/PRIOR D	CLASS <del>438</del> 257	SUBCLASS 773	ART UNIT 2812 27	EXAMINER Mitche//
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## APPLICANTS

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**TITLE**

Fabrication of a semiconductor device with air gaps for ultra-low capacitance interconnections

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent, No. _____  _____	_____ (Assistant Examiner) _____ (Date)  _____ (Primary Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			<b>ISSUE FEE</b> Amount Due      Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)		<b>ISSUE BATCH NUMBER</b>	

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